FIG. 1

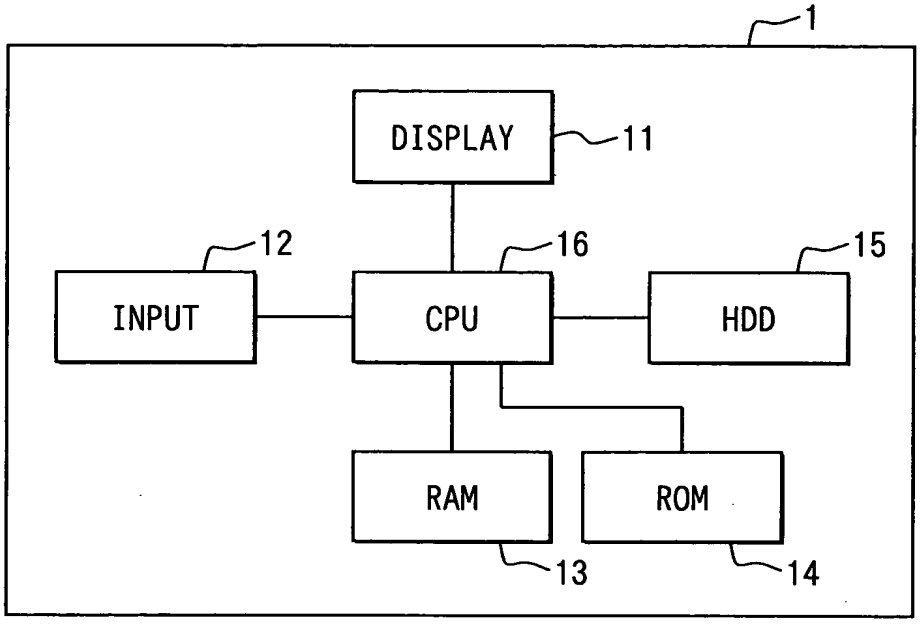
1
DISPLAY
11
12
16
15
INPUT
CPU
HDD
RAM
ROM
13
14

FIG. 2

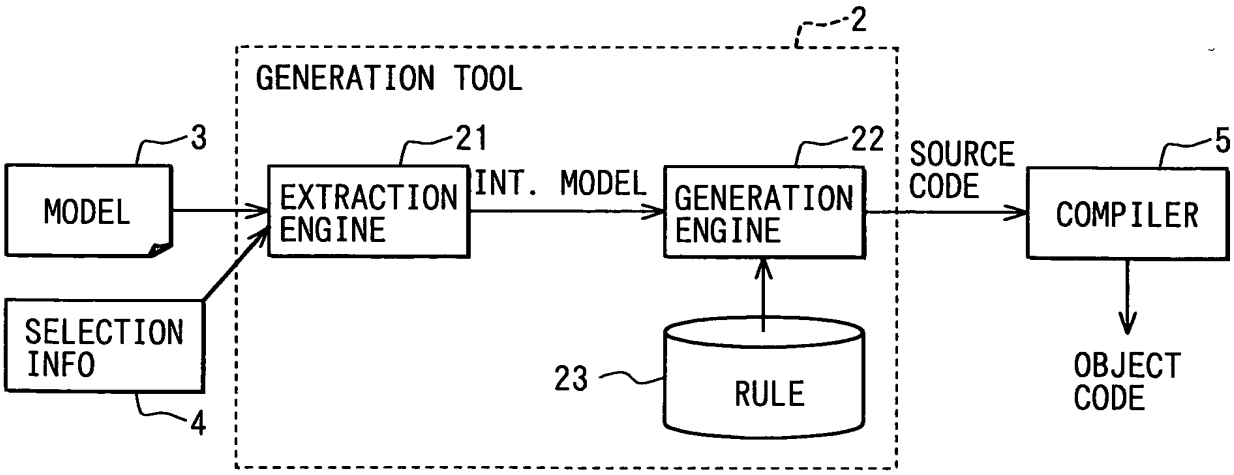
2
GENERATION TOOL
3
21
22
5
MODEL
EXTRACTION ENGINE
INT. MODEL
GENERATION ENGINE
SOURCE CODE
COMPILER
SELECTION INFO
4
23
RULE
OBJECT CODE

FIG. 3

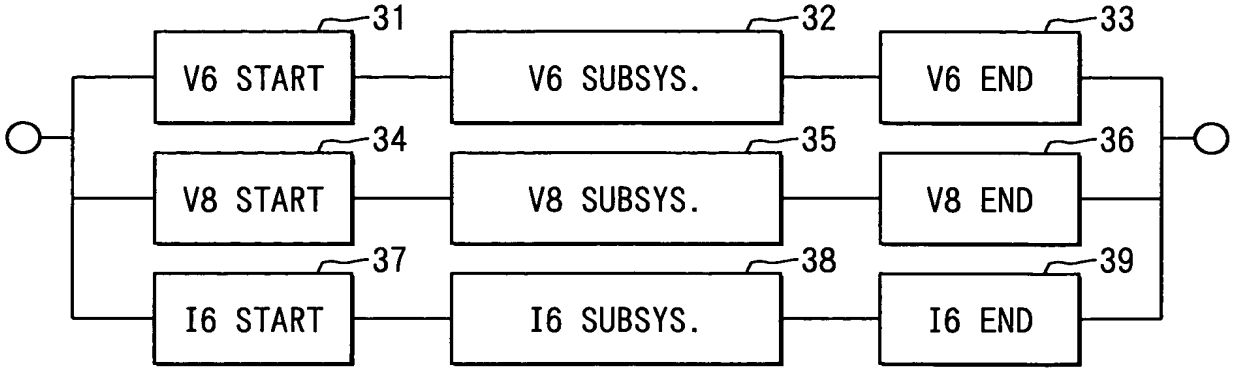

FIG. 4A

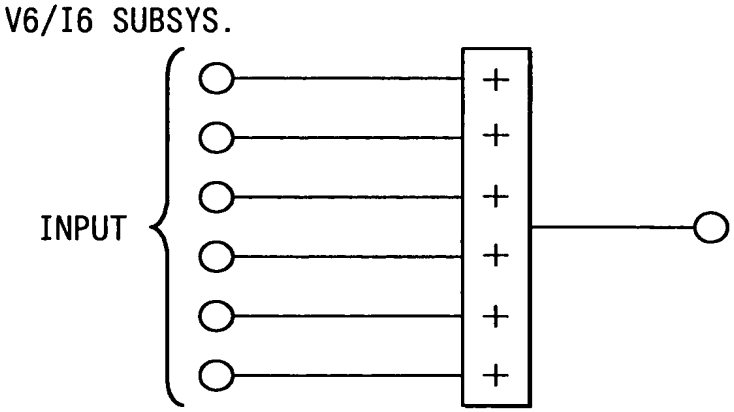

FIG. 4B

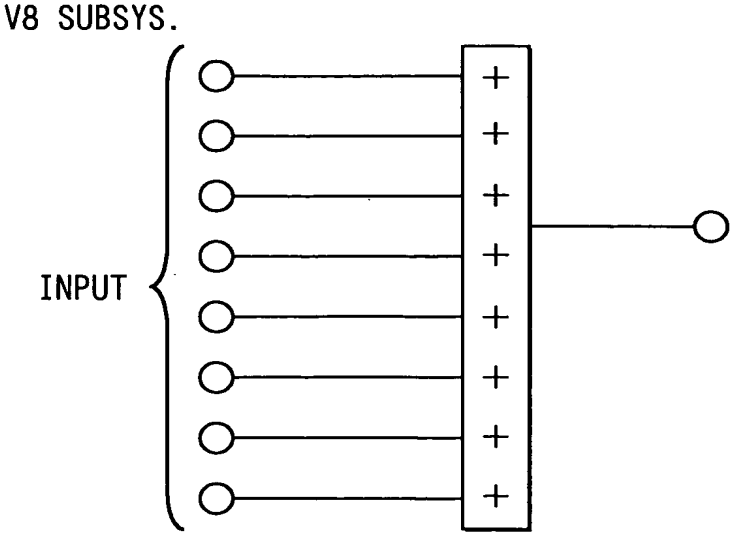

FIG. 5

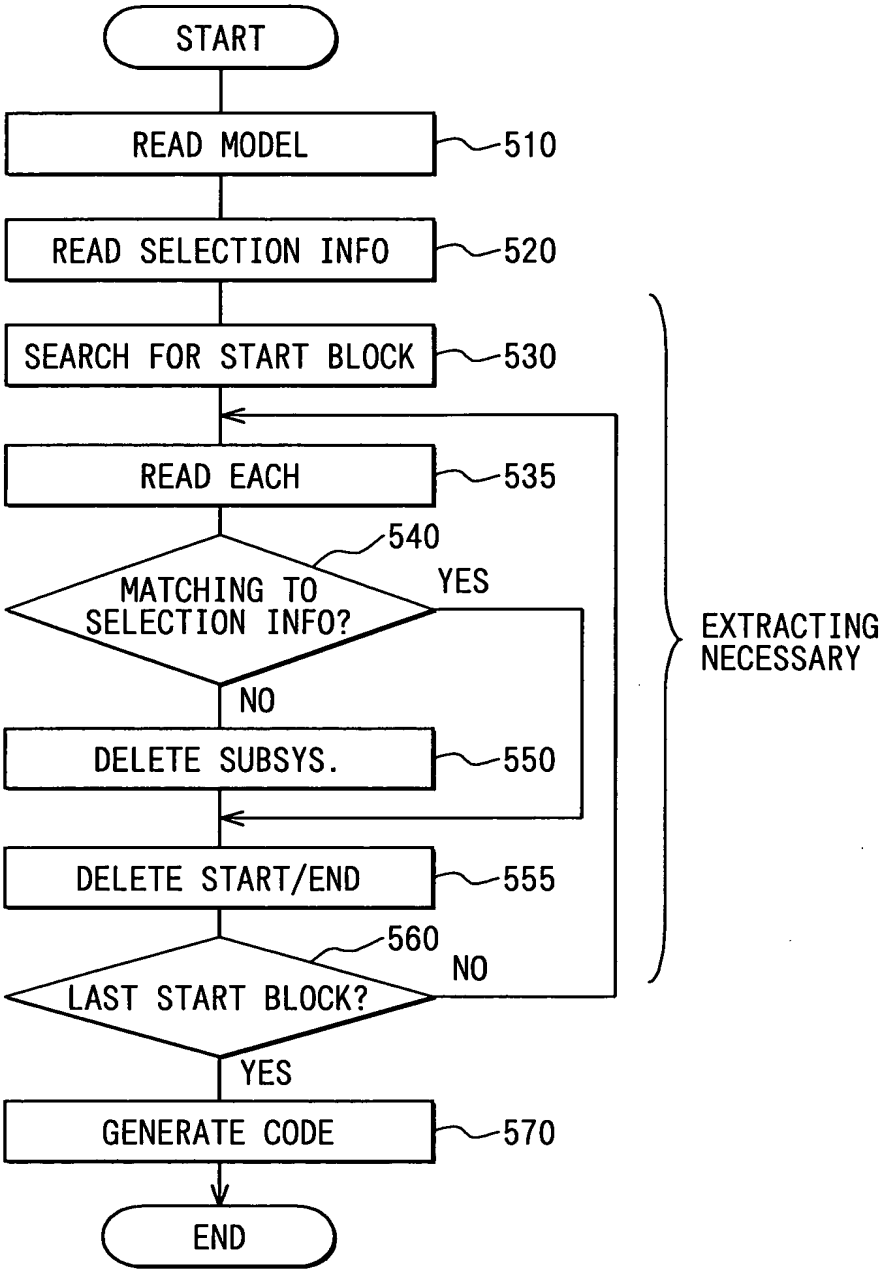
START
READ MODEL
510
READ SELECTION INFO
520
SEARCH FOR START BLOCK
530
READ EACH
535
540
MATCHING TO SELECTION INFO?
YES
NO
DELETE SUBSYS.
550
DELETE START/END
555
560
LAST START BLOCK?
NO
YES
GENERATE CODE
570
END
EXTRACTING NECESSARY

FIG. 6

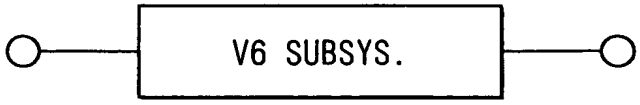
V6 SUBSYS.

FIG. 7

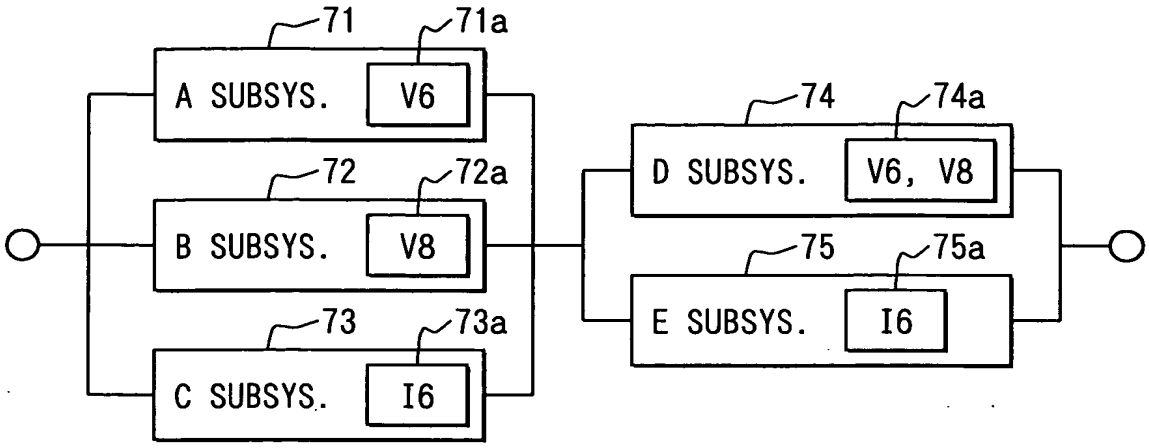
71
71a
A SUBSYS.
V6
72
72a
B SUBSYS.
V8
73
73a
C SUBSYS.
I6
74
74a
D SUBSYS.
V6, V8
75
75a
E SUBSYS.
I6

FIG. 8

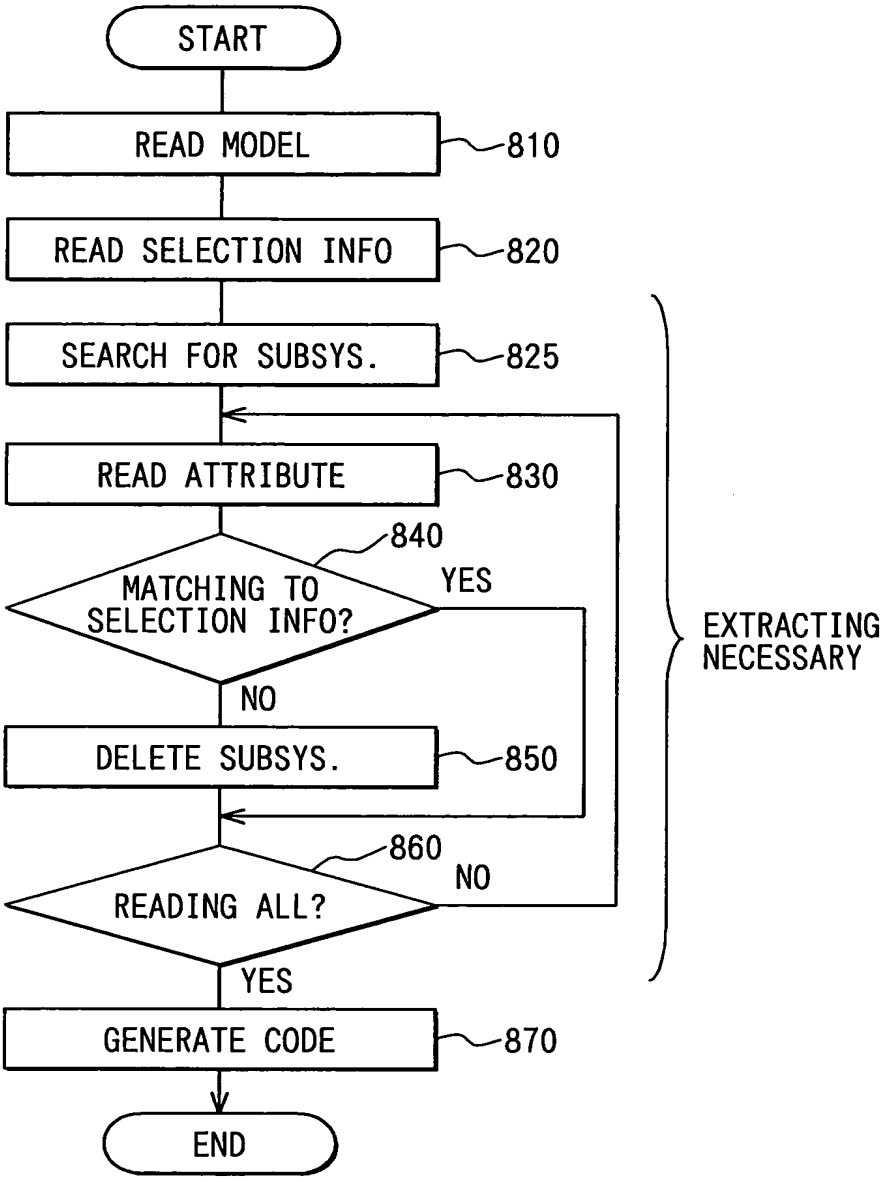
START
READ MODEL
810
READ SELECTION INFO
820
SEARCH FOR SUBSYS.
825
READ ATTRIBUTE
830
840
MATCHING TO SELECTION INFO?
YES
NO
DELETE SUBSYS.
850
860
READING ALL?
NO
YES
GENERATE CODE
870
END
EXTRACTING NECESSARY

FIG. 9

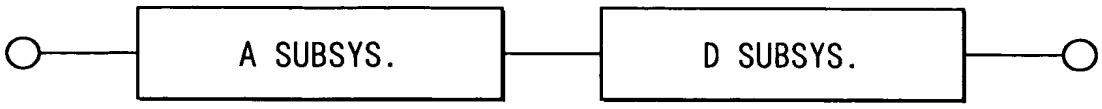
A SUBSYS.
D SUBSYS.

FIG. 10

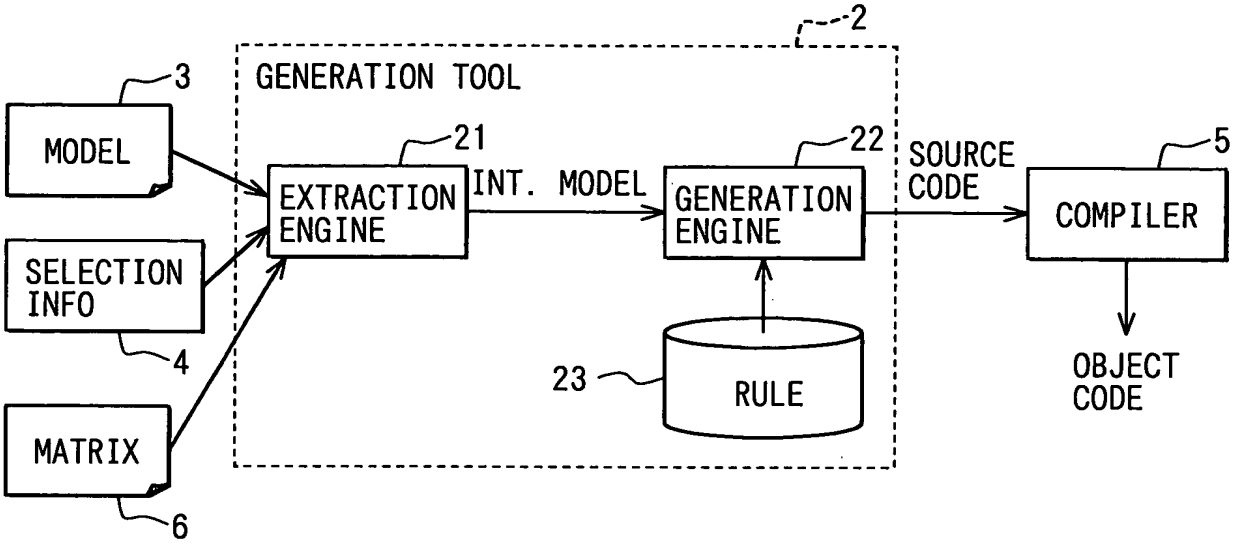

FIG. 11

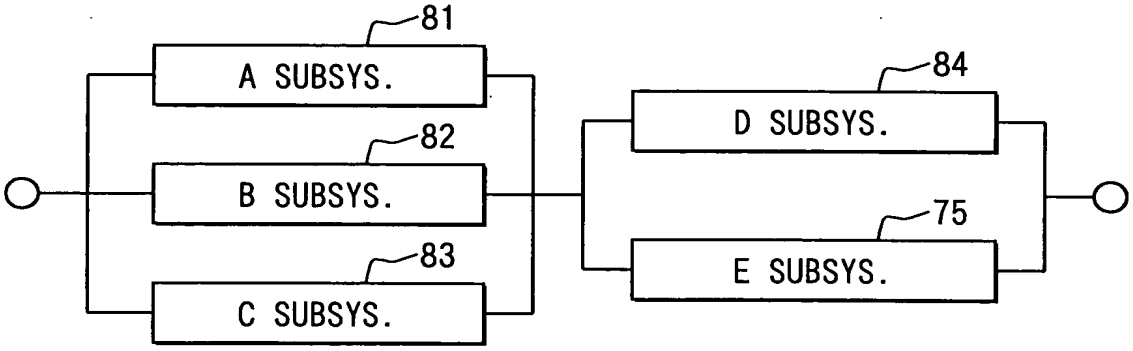

FIG. 12

	V6	V8	I6
A SUBSYS.	○		
B SUBSYS.		○	
C SUBSYS.			○
D SUBSYS.	○	○	
E SUBSYS.			○

FIG. 13

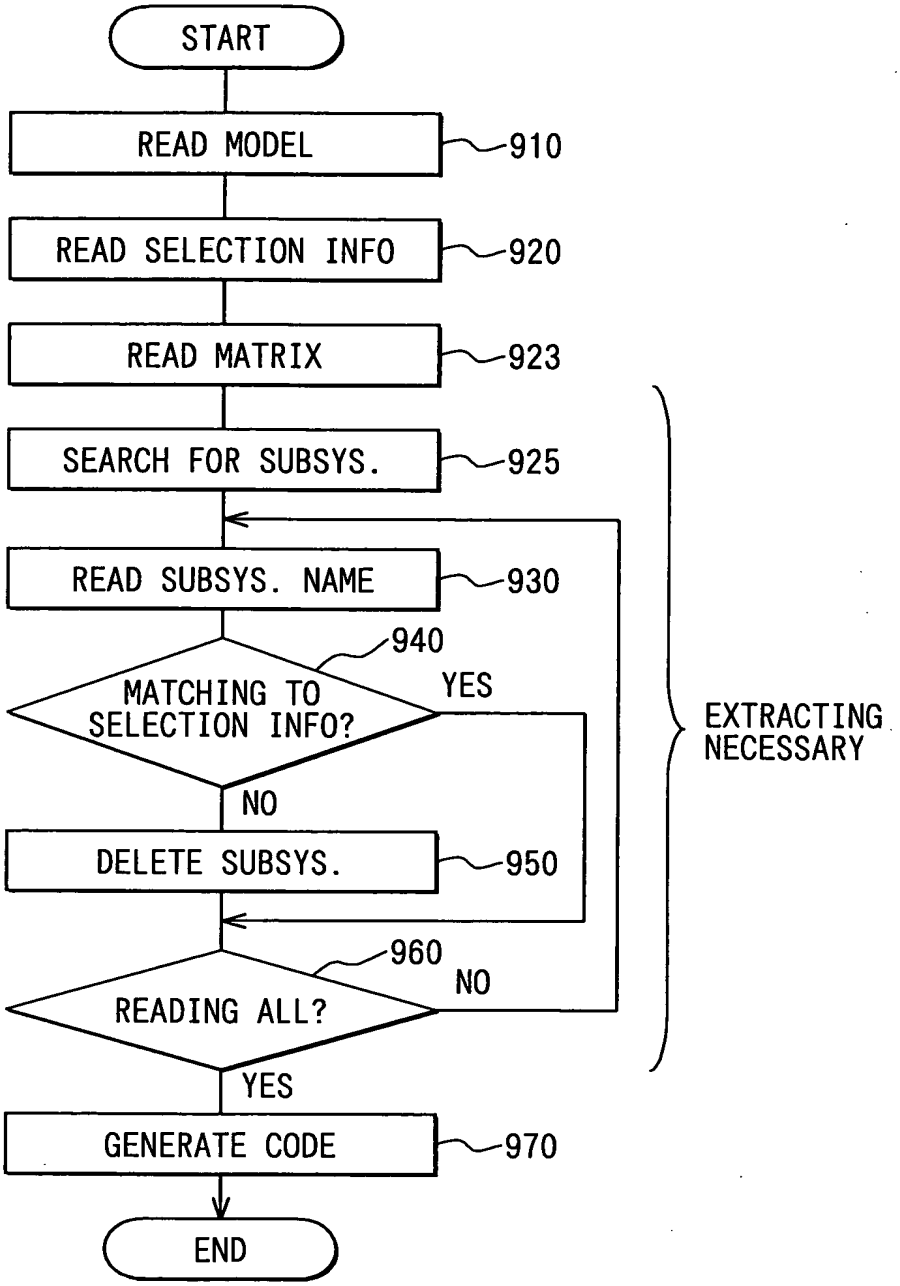
START
READ MODEL
910
READ SELECTION INFO
920
READ MATRIX
923
SEARCH FOR SUBSYS.
925
READ SUBSYS. NAME
930
940
MATCHING TO SELECTION INFO?
YES
NO
DELETE SUBSYS.
950
960
READING ALL?
NO
YES
GENERATE CODE
970
END
EXTRACTING NECESSARY

FIG. 14

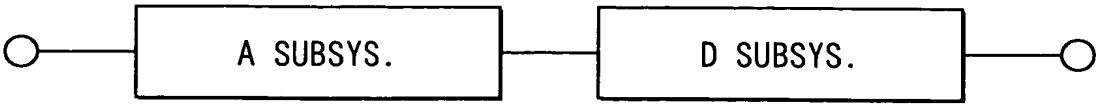
A SUBSYS.
D SUBSYS.

FIG. 15

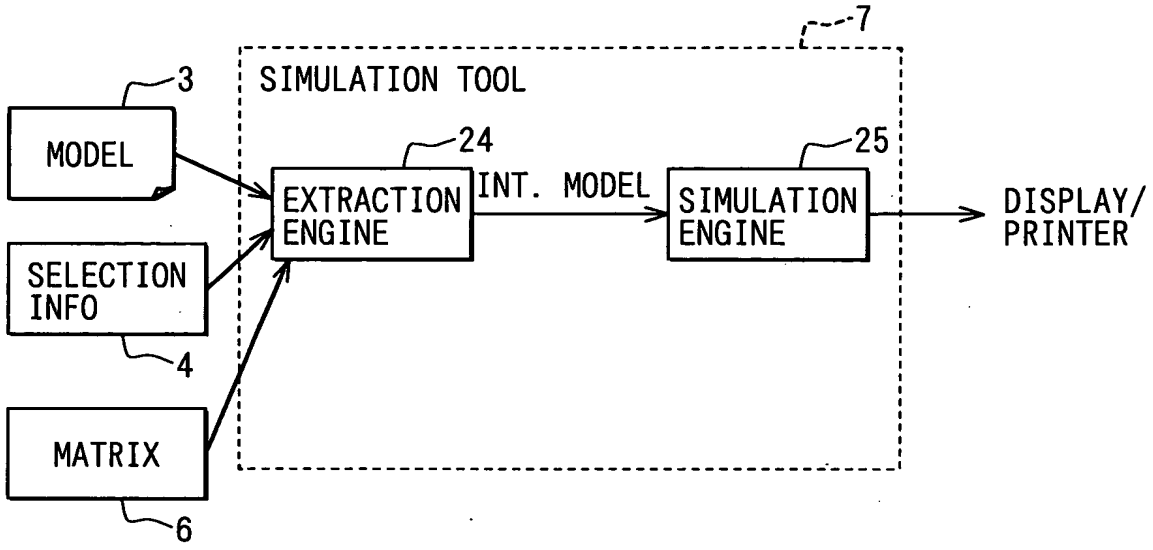

FIG. 16A

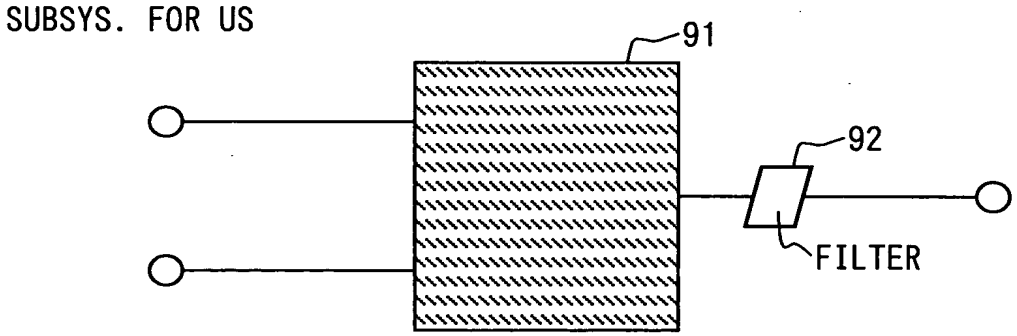

FIG. 16B

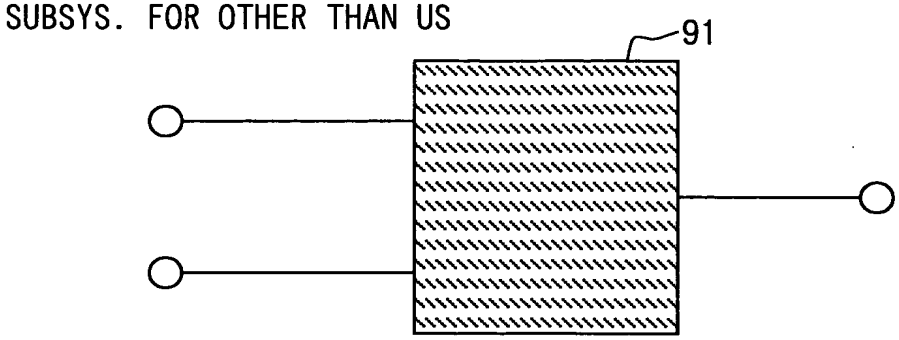

FIG. 17
RELATED ART

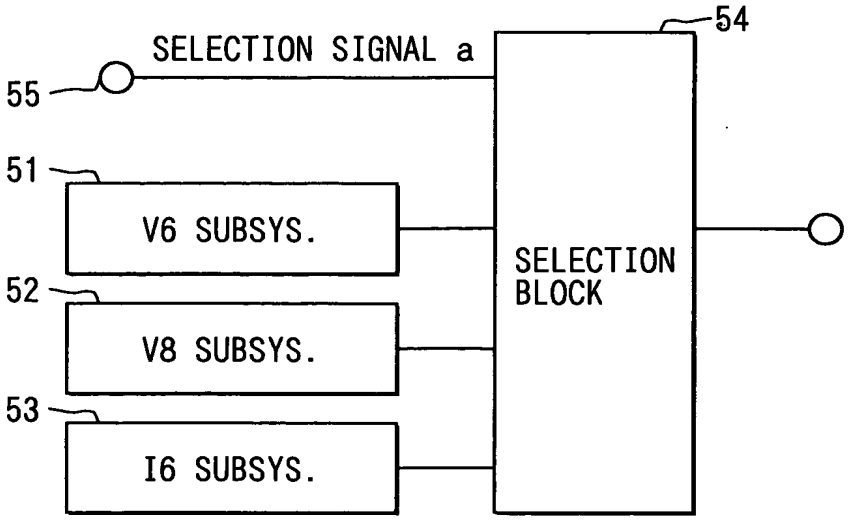

FIG. 18
RELATED ART

```
Switch (a){
 case1:
  V6 SUBSYS.
  break;
 case2:
  V8 SUBSYS.
  break;
 case3:
  I6 SUBSYS.
  break;
}
```